CPU   RAM

CPU Fetch, Decode, Execute Instruction

Currently running programs, currently used data.

Cache ROM

A buffer between CPU and Memory.

Start-up Instructions

Embedded system

Storage
Technologies

A computer system added to another type of device.

Optical
Magnetic
Solid State

Components of a CPU

Purpose of ALU

ALU	To add, subtract (do arithmetic)
Cache	And
CU	Compare (do logic).
Clock	

Purpose of Control Unit

Purpose of Clock

To send signals to other parts of the CPU.
Allows Input and Output devices to respond.

Allows accurate timing of FDE cycle.

WAN / LAN   Network Protocol

Wide Area Network – Large area not under control of organisation.
Local Area Network – Under control and ownership of organisation.

A set of rules or standards for communication

Server   SQL

Controls access to a centralised resource (files, email, print, web pages)

SELECT
FROM
WHERE

Benefits of a network

Properties of secondary storage

CAMPUS F

Communication
Access
Monitoring
Peripherals
Updates
Security
Files

SpDu PoRe CoCa

Speed
Durability
Portability
Reliability
Cost
Capacity