Subject: Electronics GCSE

Exam Board: WJEC

Your exam is 50 minutes in a lesson.

Mock paper 1: 50 minutes (not full paper)40%

Paper 1 Content:

A mix of short answer questions, structured questions and extended writing questions, with some set in a practical context based around unit 1 'Discovering Electronics'

Mock Paper 2: 50 minutes (not full paper) 40%

Paper 2 Content:

A mix of short answer questions, structured questions and extended writing questions, with some set in a practical context based around unit 2 'Application of Electronics'

Useful revision resources:

Use the knowledge organisers that have been shared on Bromcom

Website: https://resources.eduqas.co.uk/Pages/ResourceSingle.aspx?rlid=937 (Digital eBook)

Revision Tips

You will get most of the formulas on the formulas sheet but you must revise when and where to use them.

Outline revision area(s) to be covered

- RC timer & Ohms Law
- Power calculations
- Kirchoffs Law & Potential dividers
- Power calculations
- Transistor calculations regarding HFE and IC&IB
- Logic recognising gates and truth table for each gate. Some combinational logic
- 555 astable and monostable circuits.