

Subject: Electronics**Exam Board: Edexcel****Open book: Paper 1, 45 minute subject papers sat in class time**

Logic gates
Block/system diagrams
Ohms Law
Resistor networks
Operational amplifiers
Combinational logic

Invigilated assessment: Paper 2 75 minutes

Flow charts
Monostable circuits
Non-inverting op amp
Combinational logic
Schmitt invertors

Useful revision resources:

GCSE Bitesize
WJEC past papers https://www.wjec.co.uk/qualifications/electronics-gcse/#tab_pastpapers
Oak national academy (Physics section and 'Electricity')
WJEC eBook <https://resources.wjec.co.uk/Pages/ResourceSingle.aspx?rId=2352>

Revision Tips

Read over your notes in each section and make revision resources (posters, cue cards, shortened notes, spray diagrams, post it notes etc.) complete quizzes, watch videos, complete exam questions and check against the mark scheme. When using the eBook there are quizzes in every section.

Date	Electronics
22/3	Ohms law, resistor networks
29/3	System diagrams, logic and combinational logic
19/4	Op amps, monostable circuits.
26/4	Schmitt invertors, flowcharts